LEGIC

ATC256-MV410

Innovative and cost-efficient identification via ISO 15693



Secure, low-cost smartcard IC

LEGIC advant smartcard ICs offer high security at low cost. They are ideal for high volume projects and are the perfect choice for secure and competitive applications.

A competitive RFID solution

The ATC256-MV410 smartcard IC combines long reading distance, good transaction speed and a powerful, modern security architecture that enables reliable and competitive RFID solutions. It is the best choice for customers who want to cost-effectively migrate from older, less secure contactless ICs to a modern, highly secure one.

Security at a distance

The IC implements Grain 128a authentication and encryption to

provide a solid foundation for secure applications of today and tomorrow. The IC offers a higher level of security and a longer reading distance than comparable ISO/IEC 15693 products making it an ideal and economical solution.

Multiple applications

LEGICs smartcard ICs fulfill all the requirements for the hotel industry and are the preferred choice for contactless hotel keycards. They implement a special, low-power encryption technique which provides high security for hotel guests while maintaining long reading distance. They are also ideal for City Cards which are used as electronic travel, payment, customer ID as well as gift cards. The IC is equipped with sufficient memory to support multiple applications. Combined with long reading distance, the ATC256-MV410 provides a comfortable and secure user experience.



ATC256-MV410

Long reading distance and strong encryption makes the ATC256-MV410 an excellent ISO/IEC 15693 compatible IC. It achieves a security level in the ISO 15693 world that was only possible in ISO 14443 A until now. The 224-byte memory is suitable for a wide variety of applications.

Technical data

	ATC256-MV410
RF standard	ISO 15693
Memory size (Byte)	224
UID (Byte)***	8
Safe ID	Yes
Range**	Up to 70 cm
Key management (per application)	Master-Token System-Control
Data transfer encryption	Grain 128a
Data storage encryption (per application)	AES (128/256 Bit), 3DES, LEGIC encryption
Cryptographic authentication (per application)***	128 Bit
Max. number of applications*	12
Memory segmentation	Dynamic
Application segment size	Variable
Data retention (min.)	10 years
EEPROM cycles (min.)	100,000
Baud rate (kbit/s)	Up to 26.48
Delivery form	Wafer

* Memory size indications are nominal values. The effective max. number of applications is depending on the memory requirements of applied applications

** Max. reading range depends on used RF standard, the requirements of national spectrum management authorities, reader application, antenna, transponder suroundings

*** Depends on RF standard

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